

## Fundamentals Of Logic Design Solution Manual

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A = 1 and B = 1, B = 1, \u0026amp; B = 1, the latch state is Q = 1, Q = 1, Q = 1, (e) Derive a circuit for the A B AB A B latch using four two-input NOR \text{(NOR)} \u0026amp; two inverters. An A B AB \u0026amp; A B latch operates as follows: If A = 0 A = 0, \u0026amp; A = 0 and B = 0, B = 0, \u0026amp; B = 0, the latch state is Q = 0; Q = 0, \u0026amp;

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Truth tables and state tables still are used to specify the behavior of logic circuits, and Boolean algebra is still a basic mathematical tool for logic design. Even when programmable logic devices are used instead of individual gates and flip-flops, reduction of logic equations is still desirable in order to fit the equations into smaller PLDs.

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View an educator-verified, detailed solution for Chapter 18, Problem 18.28 in Kinney/Roth 's Fundamentals of Logic Design (7th Edition).

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